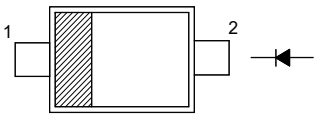
Silicon Epitaxial Planar Diode

High Voltage Switching Diode

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



Top View
Simplified outline SOD-323 and symbol

Features

- AEC-Q101 Qualified
- High reliability
- Low leakage Current
- Halogen and Antimony Free(HAF), RoHS compliant

Absolute Maximum Ratings (T_a = 25℃)

Parameter	Symbol	Value	Unit
Maximum Repetitive Reverse Voltage	V_{RRM}	300	V
Reverse Voltage	V_R	250	V
Forward Current	I_F	100	mA
Maximum Forward Current	I_{FM}	300	mA
Non-repetitive Peak Forward Surge Current at t = 10 ms	I_{FSM}	1.7	A
Power Dissipation [1)]	P_D	200	mW
Operating Junction Temperature Range	T_j	- 55 to + 150	℃
Storage Temperature Range	T_{stg}	- 55 to + 150	℃

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient [1)]	$R_{\theta JA}$	625	℃/W

[1)] Device mounted on FR-4 PCB with minimum recommended pad layout.

Characteristics at T_a = 25℃

Parameter	Symbol	Min.	Max.	Unit
Reverse Breakdown Voltage at I_R = 100 μA	$V_{(BR)R}$	250	-	V
Forward Voltage at I_F = 100 mA	V_F	-	1.2	V
Reverse Current at V_R = 250 V at V_R = 300 V	I_R	- -	0.2 100	μA
Total Capacitance at V_R = 0 V, f = 1 MHz	C_{tot}	-	3	pF
Reverse Recovery Time at I_F = I_R = 30 mA, I_{rr} = 3 mA，R_L = 100 Ω	t_{rr}	-	100	ns

Electrical Characteristic Curve

Fig 1. Power Derating Curve

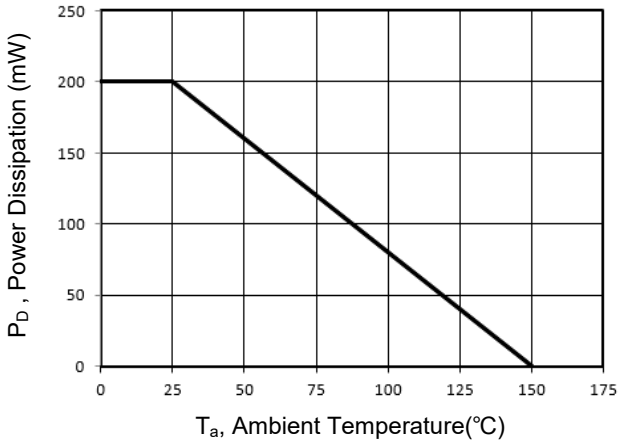


Fig 2. Forward Characteristic Curve

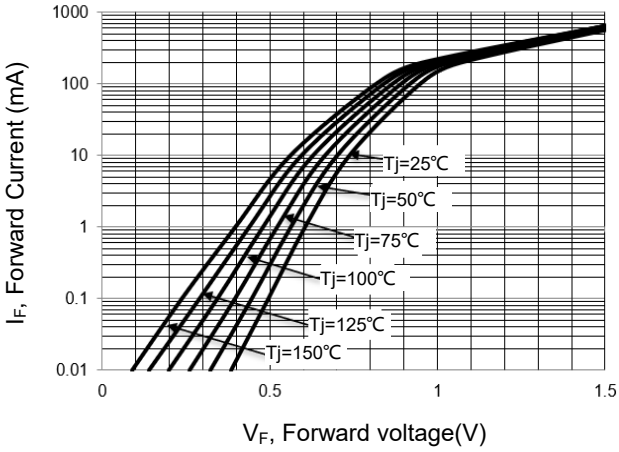


Fig 3. Reverse Characteristic Curve

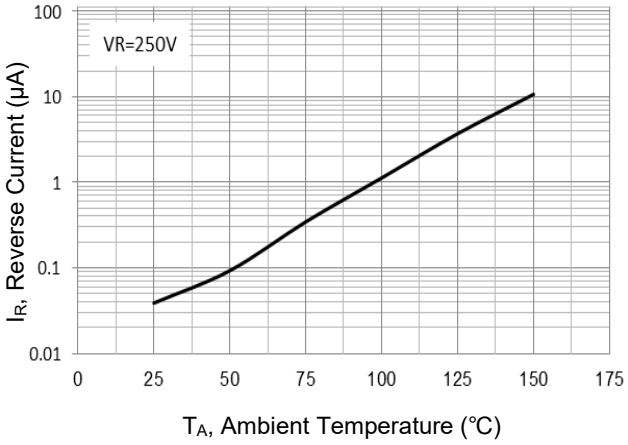
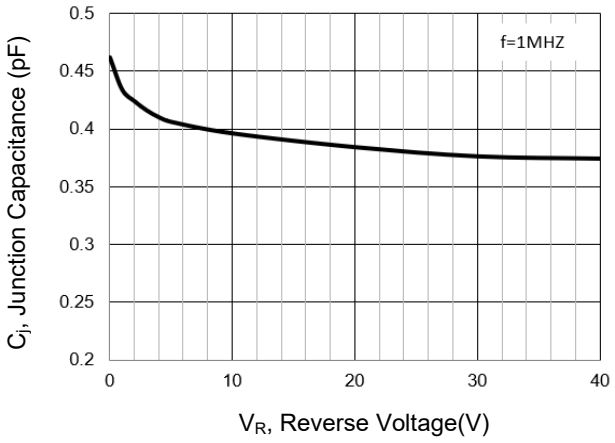
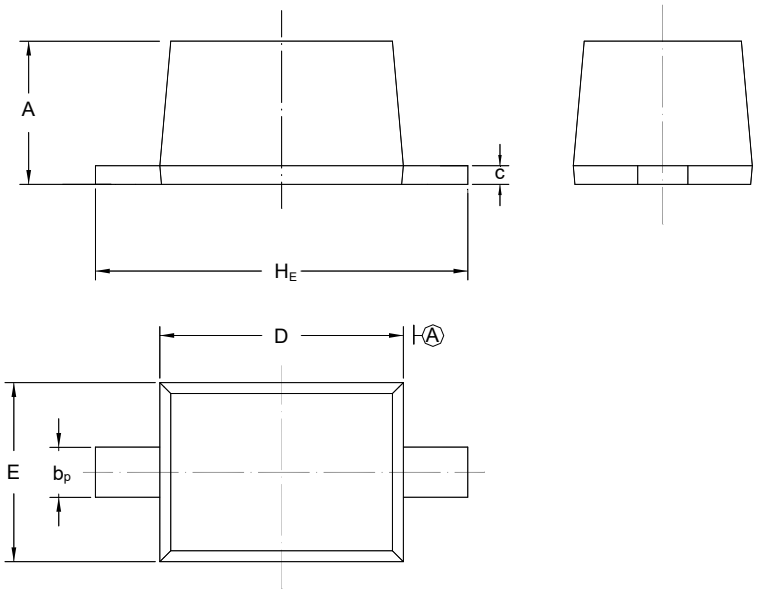


Fig 4. Junction Capacitance



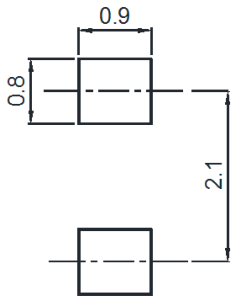
PACKAGE OUTLINE

Plastic surface mounted package SOD-323



UNIT	A	b_p	C	D	E	H_E
mm	1.10 0.80	0.40 0.25	0.15 0.10	1.80 1.60	1.35 1.15	2.80 2.30

Recommended Soldering Footprint

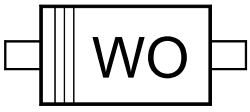


Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOD-323	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

" WO " = Part No.
" III " = Cathode line
Font type: Arial



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