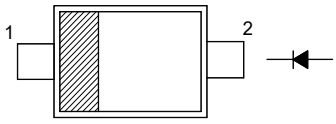
Silicon Epitaxial Planar Switching Diode

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



Top View
Simplified outline SOD-323 and symbol

Features

- AEC-Q101 Qualified
- Halogen and Antimony Free(HAF), RoHS compliant

Absolute Maximum Ratings (T_a = 25℃)

Parameter	Symbol	Value	Unit
Reverse Voltage	V_R	70	V
Forward Current	I_F	200	mA
Forward Continuous Current	I_{FM}	500	mA
Power Dissipation [1)]	P_D	200	mW
Operating Junction Temperature Range	T_j	- 55 to + 150	℃
Storage Temperature Range	T_{stg}	- 55 to + 150	℃

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient [1)]	$R_{\theta JA}$	625	℃/W

[1)] Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.

Electrical Characteristics (T_a = 25℃)

Parameter	Symbol	Min.	Max.	Unit
Reverse Breakdown Voltage at I_R = 100 μA	V_{BR}	70	-	V
Forward Voltage at I_F = 1 mA at I_F = 100 mA	V_F	 0.55 0.85	 0.7 1.1	V
Reverse Current at V_R = 50 V	I_R	-	0.1	μA
Total Capacitance at V_R = 0 V, f = 1MHz	C_T	-	2.5	pF
Reverse Recovery Time at I_F = 10 mA, I_{rr} = 0.1 X I_R, V_R = 6 V, R_L = 100 Ω	t_{rr}	-	4	ns

Electrical Characteristics Curves

Fig 1. Power Derating Curve

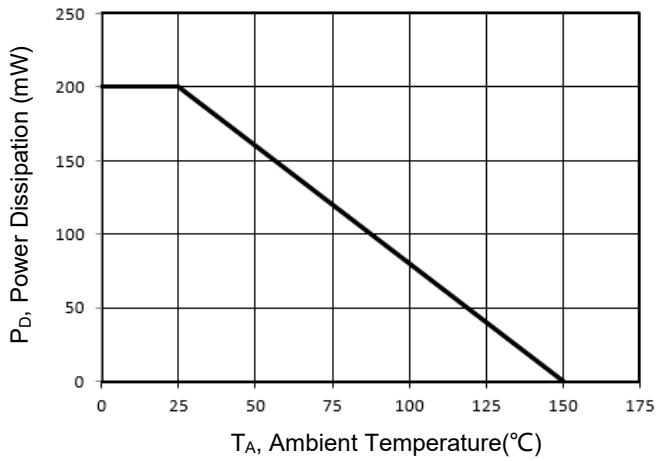


Fig 2. Total Capacitance vs. Reverse Voltage

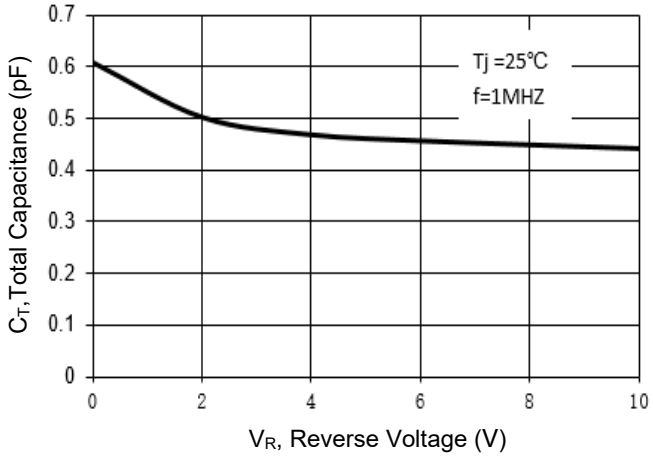


Fig 3. Reverse Current vs. Reverse Voltage

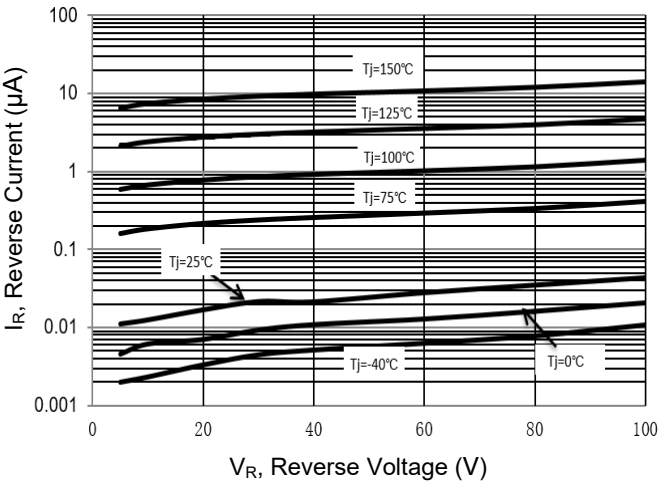
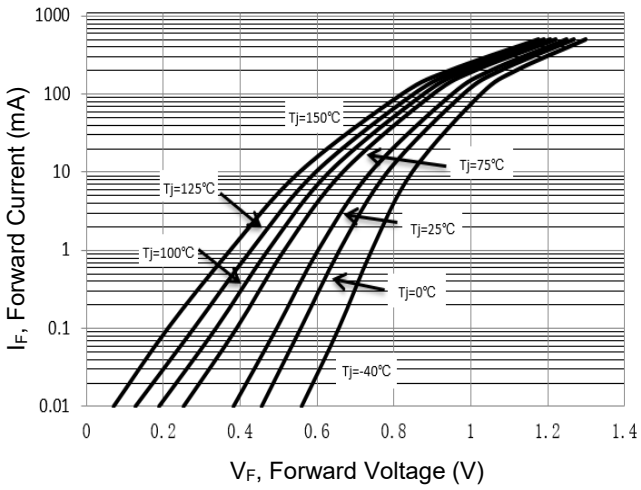
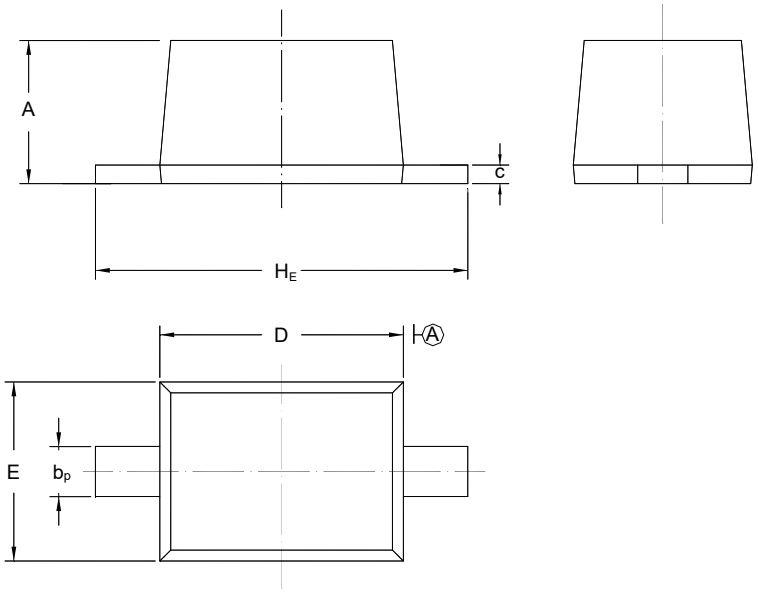


Fig 4. Forward Characteristics



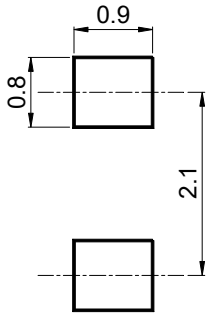
PACKAGE OUTLINE

Plastic surface mounted package SOD-323



UNIT	A	b_p	C	D	E	H_E
mm	1.10 0.80	0.40 0.25	0.15 0.10	1.80 1.60	1.35 1.15	2.80 2.30

Recommended Soldering Footprint

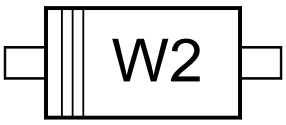


Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOD-323	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

" W2 " = Part No.
" III " = Cathode line
Font type: Arial



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